

Certificate of compliance

Applicant: SolarEdge Technologies Ltd.

1 HaMada Street Herzeliya 4673335

Israel

Product: Compact photovoltaic (PV) inverter

Model: SE1000M

SE1500M SE2000M SE2500M

Use in accordance with regulations:

Automatic disconnection device with single-phase mains surveillance in accordance with Engineering Recommendation G98/NI for photovoltaic systems with a single-phase parallel coupling via an inverter in the public mains supply. The automatic disconnection device is an integral part of the aforementioned inverter. This serves as a replacement for the disconnection device with isolating function that can access the distribution network provider at any time.

Applied rules and standards:

Engineering Recommendation G98/NI-1:2019

Requirements for the connection of Fully Type Tested Micro-generators (up to and including 16 A per phase) in parallel with public Low Voltage Distribution Networks in North Ireland

DIN V VDE V 0126-1-1:2006-02 (4.1 Functional safety)

Automatic disconnection device between a generator and the public low-voltage grid

At the time of issue of this certificate the safety concept of an aforementioned representative product corresponds to the valid safety specifications for the specified use in accordance with regulations.

Report number: 17TH0251-G98/NI_1 Certification program: NSOP-0032-DEU-ZE-V01

Certificate number: U20-0095 Date of issue: 2020-02-25



Certification body Bureau Veritas Consumer Products Services Germany GmbH accredited according to DIN EN ISO/IEC 17065

A partial representation of the certificate requires the written approval of Bureau Veritas Consumer Products Services Germany GmbH



Appendix C Type Test Verification Report

Extract from test report according to the Engineering Recommendation G98/NI

Nr. 17TH0251-G98/NI 1

Type Approval and declaratio	n of compliance with the	e requirements of Engi	ineering Recom	mendatio	n G98/NI.		
Micro-generator Technology	Compact photovoltaic in	Compact photovoltaic inverter					
Manufacturer	SolarEdge Technologies	Ltd.					
Address	1 HaMada Street Herzeliya 4673335 Israel						
Tel	+972-9-957-6620	Fax		+972-9-9	57-6591		
Email	info@solaredge.com	Website		www.sola	aredge.com		
Rated values	SE1000M	SE1500M	SE2000	M	SE2500M		
Maximum rated capacity	1,0 kW	1,5 kW	2,0 kW		2,5 kW		
Rated voltage	220/230 60Hz/50Hz						
Firmware version	Main DSP: Version 1.00 Aux DSP: Version 2.01						
Measurement period:		2017-12-10 to 2018-02-20 2019-01-10 to 2019-02-05 2019-05-16					

Description of the structure of the power generation unit:

The power generation unit is equipped with a PV and line-side EMC filter. The power generation unit has no galvanic isolation between DC input and AC output. Output switch-off is performed with single-fault tolerance based on two series-connected relays in line and neutral. This enables a safe disconnection of the power generation unit from the network in case of error.

Differences between Generating Units:

The inverters of the SExxxxM series consist of the following power models: SE2500M, SE2000M, SE1500M and SE1000M. All the models use the same hardware and software. The different powers are realized by software derating. Therefore the test results of the SE2000M model are valid for the whole series.

The above stated Generating Units are tested according the requirements in the Engineering Recommendation G98/NI. Any modification that affects the stated tests must be named by the manufacturer/supplier of the product to ensure that the product meets all requirements of the Engineering Recommendation G98/NI.



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Operating Range.				
Connection:	Always connected			
Limit:	Always connected			
Voltage = 85% of nominal (195,5 V) Frequency = 47.5 Hz Power Factor = 1 Period of test 90 minutes				
Connection:	Always connected			
Limit:	Always connected			
Test 2	Voltage = 110% of nominal (253 V) Frequency = 51.5 Hz Power Factor = 1 Period of test 90 minutes			
Connection:	Always connected			
Limit:	Always connected			
Voltage = 110% of nominal (253 V) Frequency = 52.0 Hz Power Factor = 1 Period of test 15 minutes				
Connection:	Always connected			
Limit:	Always connected			

Protection. Voltage tests.									
Phase 1									
Function	Set	ting	Trip	test	No trip	test			
	Voltage [V]	Time delay [s]	Voltage [V]	Time delay [s]	Voltage / time	Confirm no trip			
U/V stage 1	185,5	3,0	194,5	3,206	199,5V / 5s	No trip			
U/V stage 2	138,0	2,0	137,4	2,206	142V / 2,5s	No trip			
					134V / 1,98s	No trip			
O/V stage 1	253,0	0,5	252,5	0,699	249V 5,0s	No trip			
					257V 0,45s	No trip			

Note. For Voltage tests the Voltage required to trip is the setting $\pm 3,45$ V. The time delay can be measured at a larger deviation than the minimum required to operate the protection. The No trip tests need to be carried out at the setting ± 4 V and for the relevant times as shown in the table above to ensure that the protection will not trip in error.



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Protection. Frequency tests.								
Function	tion Setting		Trip	test	No trip test			
	Frequency [Hz]	Time delay [s]	Frequency [Hz]	Time delay [s]	Frequency / time	Confirm no trip		
U/F stage 1	48,0	0,5	48,0	0,719	48,2Hz / 25s	No trip		
					47,8Hz / 0,45s	No trip		
O/F stage 1	52,0	1,0	52,0	1,213	51,8Hz / 120s	No trip		
					52,2Hz / 0,98s	No trip		

Note. For Frequency Trip tests the Frequency required to trip is the setting ± 0.1 Hz. In order to measure the time delay a larger deviation than the minimum required to operate the projection can be used. The "No-trip tests" need to be carried out at the setting ± 0.2 Hz and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

Protection. Loss of Mains.								
Inverters tested according to BS EN 62116.								
Balancing load on islanded network	33% of -5% Q Test 22	66% of -5% Q Test 12	100% of -5% P Test 5	33% of +5% Q Test 31	66% of +5% Q Test 21	100% of +5% P Test 10		
Trip time. Ph1 fuse removed [s]	0,093	0,080	0,113	0,119	0,115	0,126		



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D	D	
Drotoction	Re-connection	timar
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Test should prove that the reconnection sequence starts in no less than 20 seconds for restoration of voltage and frequency to

within the stage 1 settings of table		11 110 163	5 11an 20 56001	ids for restoration of void	age and nequency to	
	C	Over Vo	tage			
Time delay	setting			Measured delay		
60s	3			69s		
	U	nder Vo	ltage			
Time delay	setting			Measured delay		
60s	3			72s		
	Ov	er Freq	uency			
Time delay	setting		Measured delay			
60s	3		76s			
	Und	der Fre	quency			
Time delay	setting			Measured delay		
60s	3		71s			
	Checks on no reconnection when voltage or frequency is brought to just outside stage of table 1.				outside stage 1 limits	
	At 257,0V		At 191,5V	At 47,9Hz	At 52,1Hz	
Confirmation that the Generating Unit does not reconnect.	No reconnection	No	reconnection	No reconnection	No reconnection	

Protection. Frequency change, Stability test.							
	Start Frequency [Hz]	Change	Test Duration	Confirm no trip			
Positive Vector Shift	49,5	+50 degrees		No trip			
Negative Vector Shift	50,5	-50 degrees		No trip			
Positive Frequency drift	49,0 to 51,0	+0,95Hz/sec	2,1s	No trip			
Negative Frequency drift	51,0 to 49,0	-0,95Hz/sec	2,1s	No trip			



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Limited Frequency Sensitive Mode – Over Frequency								
1-min mean value [Hz]:	a) 50,00	b) 50,25	c) 50,70	d) 51,15	e) 50,70	f) 50,25	g) 50,00	
1. Measurement a) to g): Active power output > 80% Pn								
Frequency [Hz]:	N/A	1,96	1,51	1,05	1,51	1,96	N/A	
P _{expected} [kW]:	2,01	1,96	1,50	1,06	1,51	1,97	2,01	
P _{measured} [kW]:	N/A	0,00	0,00	0,00	0,00	0,01	N/A	
2. Measurement a) to g): Active	power outpu	t 40% and 60	% after freezi	ng > 80% Pn				
Frequency [Hz]:	N/A	0,96	0,74	0,52	0,74	0,96	N/A	
P _{expected} [kW]:	0,99	0,96	0,74	0,52	0,74	0,97	1,03	
Pmeasured [kW]:	N/A	0,00	0,00	0,00	0,00	0,00	N/A	

Output Power with falling Frequency						
Frequency setpoint [Hz]:	50,00	47,60	47,10			
Frequency [Hz]:	50,00	49,55	47,55			
Active power [kW]:	1981	1980	1879			
ΔP/PM [%] per 1 Hz:			0			





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Power Quality. Harmonics.

SE2500M

Phase 1

SSE	G rating per phase	(rpp)				
	At 45-55% of 1,21	-		ited output DkW		
Harmonic	Measured Value (MV) in [A]	Measured Value (MV) in [%]	Measured Value (MV) in [A]	Measured Value (MV) in [%]	Limit in BS EN61000-3-2 in Amps	Higher limit for odd harmonics 21 and above
2nd	0,022	0,206	0,011	0,100	1,080	
3rd	0,075	0,696	0,073	0,679	2,300	
4th	0,014	0,129	0,011	0,104	0,430	
5th	0,026	0,243	0,029	0,271	1,140	
6th	0,011	0,098	0,012	0,109	0,300	
7th	0,024	0,223	0,022	0,206	0,770	
8th	0,011	0,105	0,012	0,108	0,230	
9th	0,022	0,208	0,027	0,253	0,400	
10th	0,011	0,098	0,012	0,112	0,184	
11th	0,022	0,206	0,026	0,237	0,330	
12th	0,011	0,104	0,013	0,118	0,153	
13th	0,023	0,217	0,023	0,212	0,210	
14th	0,011	0,104	0,012	0,109	0,131	
15th	0,023	0,214	0,023	0,216	0,150	
16th	0,012	0,112	0,013	0,119	0,115	
17th	0,024	0,227	0,027	0,249	0,132	
18th	0,013	0,117	0,014	0,133	0,102	
19th	0,024	0,219	0,026	0,240	0,118	
20th	0,014	0,131	0,015	0,141	0,092	
21th	0,023	0,217	0,023	0,213	0,107	0,160
22th	0,013	0,122	0,014	0,134	0,084	
23th	0,024	0,221	0,023	0,217	0,098	0,147
24th	0,013	0,120	0,015	0,138	0,077	
25th	0,024	0,224	0,024	0,226	0,090	0,135
26th	0,012	0,113	0,014	0,131	0,071	
27th	0,023	0,212	0,025	0,231	0,083	0,124
28th	0,012	0,108	0,013	0,123	0,066	
29th	0,022	0,203	0,023	0,210	0,078	0,117
30th	0,010	0,097	0,012	0,109	0,061	
31th	0,019	0,179	0,018	0,169	0,073	0,109
32th	0,009	0,087	0,011	0,098	0,058	
33th	0,017	0,154	0,017	0,157	0,068	0,102
34th	0,008	0,076	0,010	0,088	0,054	
35th	0,016	0,152	0,018	0,165	0,064	0,096
36th	0,008	0,074	0,009	0,079	0,051	
37th	0,016	0,146	0,015	0,144	0,061	0,091
38th	0,007	0,066	0,007	0,069	0,048	
39th	0,014	0,129	0,014	0,127	0,058	0,087
40th	0,006	0,056	0,007	0,067	0,046	

Note the higher limits for odd harmonics 21 and above are only allowable under certain conditions, if these higher limits are utilised please state the exemption used as detailed in part 6.2.3.4 of BS EN 61000-3-2 in the box below.



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Power Quality. Pov	Power Quality. Power factor.								
Output power	216,2V	230V	253V	Measured at three voltage levels and at full					
20%	0,995	0,993	0,989	output. Voltage to be maintained within ±1,5% the stated level during the test.					
50%	0,999	0,998	0,996	J					
75%	0,999	0,999	0,999						
100%	0,999	0,999	0,999						
Limit	>0,95	>0,95	>0,95						

Power Quality. Voltage fluctuation and Flicker.										
	Starting			Stopping				Running		
	dmax	d	С	d(t)	dmax	d	lc	d(t)	Pst	Plt 2 hours
Measured values at test impedance	1,195%	0,99	96%	0,0%	1,195%	0,99	96%	0,0%	0,213	0,166
Values at standard impedance										
Limits set under BS EN 61000-3-3	4%	3,3	3%	3,3% 500ms	4%	3,3	3%	3,3% 500ms	1,0	0,65
Test impedance	R			0,4	Ω		XI		0,25	Ω
	Z	Z		0,472	Ω	Ω				
Standard impedance	R			0,4	Ω	Ω		XI	0,25	Ω
	Z	Z		0,472	Ω					

Power Quality. DC injection.						
Test level power [%]	20	50	75	100		
Recorded value [mA]	10,06	7,79	1,0	9,59		
Recorded value [%]	0,09	0,07	0,01	0,09		
Limit [%]	0,25	0,25	0,25	0,25		



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Fault level Contribution.					
For a directly coup	For a Inverter SSEG				
Parameter	Symbol	Value	Time after fault	Volts [V]	Amps [A]
Peak Short Circuit current	Ip	N/A	20ms	51,1	12,7
Initial Value of aperiodic current	А	N/A	100ms	34,97	9,63
Initial symmetrical short-circuit current*	l _k	N/A	250ms	31,91	9,64
Decaying (aperiodic) component of short circuit current*	i _{DC}	N/A	500ms	30,86	10,16
Reactance/Resistance Ratio of source*	X/R	N/A	Time to Trip [s]	0,606	

For rotating machines and linear piston machines the test should produce a 0s – 2s plot of the short circuit current as seen at the Generating Unit terminals.

^{*} Values for these parameters should be provided where the short circuit duration is sufficiently long to enable interpolation of the plot.

Self Monitoring – Solid state switching.	N/A
It has been verified that in the event of the solid state switching device failing to disconnect the Generating Unit, the voltage on the output side of the switching device is reduced to a value below 50 volts within 0,5 seconds.	

Note. Unit do not provide solid state switching relays. In case the semiconductor bridge is switched off, then the voltage on the output drops to 0. In this case the relays on the output will also open (Functional safety of the internal automatic disconnection device according to VDE 0126-1-1).

Logic Interface (input port) Required by paragraph 11.1.3	Р
Confirm that an input port is provided and can be used to shut down the module.	Yes